SuVolta Raises $10.6 Million in Corporate and Venture Funding

Funding to Accelerate Integration of Low-Power Chip Technology for Internet of Things, DRAM and Mobile Applications

LOS GATOS, Calif., January 14, 2014 – SuVolta, Inc., a developer of scalable semiconductor technologies for low-power, high-performance integrated circuits (ICs), today announced that it has secured $10.6 million in funding. New investor Fujitsu Semiconductor Limited participated in the round along with existing investors Kleiner Perkins Caufield & Byers (KPCB), August Capital, New Enterprise Associates (NEA), Northgate Capital and DAG Ventures. SuVolta will use the funding to accelerate the integration of its low-power chip technology into the design and fabrication of semiconductor integrated circuits (ICs) for ultra-low power applications, such as DRAM, Internet of Things (IoT) and mobile computing.

“As we move towards an increasingly connected world, reducing power consumption and controlling costs are the semiconductor industry’s greatest challenges,” said Forest Baskett, SuVolta Board member and general partner at NEA. “SuVolta is solving these challenges while enhancing the industry’s most cost-effective process technology – planar, bulk CMOS – which is critical for the emerging Internet of Things market.”

“Fujitsu Semiconductor is committed to advancing the development of energy efficient products for the consumer and mobile markets,” said Haruyoshi Yagi, Corporate Senior Executive Vice President at Fujitsu Semiconductor Limited. “Our investment in SuVolta is a reflection of the excellent working relationship between the companies and our confidence in the value of DDC technology at a variety of process nodes.”

SuVolta develops and licenses transistor and design technologies that enable optimum power and performance of ICs. Since SuVolta’s technology uses planar, bulk CMOS, it can provide significant power-performance improvements to CMOS-based logic IC’s from 90nm to 20nm, as well as DRAM ICs. DDC technology offers designers flexibility to develop low-power improved products across a wide range of IC product types, including processors, memories, and SOCs that are critical to today’s mobile systems.

“This funding demonstrates the excitement surrounding our technology and the benefits it can bring large markets such as DRAM, IoT and mobile computing,” said Bruce McWilliams, president and CEO of SuVolta. “With DDC technology now in volume production, we are seeing increased interest from leading foundries and semiconductor companies seeking a competitive advantage through ultra-low-power optimization.”

About SuVolta, Inc.
SuVolta, Inc. develops and licenses scalable semiconductor technologies for low-power, high-performance integrated circuits. Based in Silicon Valley, the team includes world-class engineers and scientists with a long history of technology development and innovation to advance the semiconductor industry. The company is backed by Fujitsu Semiconductor Limited and by leading venture capital firms Kleiner Perkins Caufield & Byers (KPCB), August Capital, NEA, Bright Capital, Northgate Capital and DAG Ventures. For more information, visit www.suvolta.com.

To learn more about SuVolta’s technology, visit us at www.suvolta.com/technology/technology-overview/.

For information on licensing SuVolta’s technology, please go to www.suvolta.com/sales-inquiry/.

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Media Contacts:
SuVolta, Inc.
Margo Westfall
Director, Marketing Communications
+1 (408) 429 6058
mwestfall@suvolta.com

The Hoffman Agency
Steve Jursa
+1 (408) 975 3029
sjursa@hoffman.com

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